



Customer No. 22,852  
Attorney Docket No.: 04329.3216

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
)  
**Tomoko AKASHI** ) Group Art Unit: **2811**  
)  
Serial No.: **10/751,398** ) Examiner: **Not Yet Assigned**  
)  
Filed: **January 6, 2004** )  
)  
For: **SEMICONDUCTOR DEVICE AND** )  
**METHOD OF ASSEMBLING THE SAME** )

**MAIL STOP PATENT APPLICATION**  
**Commissioner for Patents**  
**P.O. Box 1450**  
**Alexandria, VA 22313-1450**

Sir:

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

**UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicant brings to the Examiner's attention the document listed on attached Form PTO-1449. Copies of the listed document is attached. Applicant respectfully requests that the Examiner consider the documents listed on attached Form PTO-1449 and indicate that they were considered by making an appropriate notation on this form. This Supplemental Information Disclosure Statement is being filed with the above-referenced application.

The following is a concise statement of relevance of the non-English language document:

1. <http://www.intel.co.jp/ip/products/desktop/processors/pentium4/index.htm>, showing the website page of Intel ® Products – Intel ® Pentium ® 4 processor, including information about a micro flipchip ball grid array.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and applicant determines that the cited documents do not constitute "prior art" under United States law, applicant reserves the right to present to the office the relevant facts and law regarding the appropriate status of such documents. Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.


If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

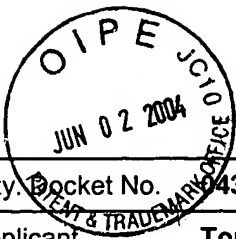
FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: June 2, 2004

By: \_\_\_\_\_

  
Richard V. Burgujian  
Reg. No. 31,744

Enclosures  
RVB/FPD/sci



## SUPPLEMENTAL INFORMATION DISCLOSURE CITATION

Atty. Pocket No. 4329.3216	Serial No. 10/751,398
Applicant Tomoko AKASHI	
Filing Date January 6, 2004	Group: 2811

## U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate

## FOREIGN PATENT DOCUMENTS

Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	<a href="http://www.intel.co.jp/jp/products/desktop/processors/pentium4/index.htm">http://www.intel.co.jp/jp/products/desktop/processors/pentium4/index.htm</a> , retrieved on April 22, 2004.
	<a href="http://www.intel.com/support/processors/sb/cs-009864.htm">http://www.intel.com/support/processors/sb/cs-009864.htm</a> , retrieved on April 21, 2004.

Examiner	Date Considered
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce